

In the Specification:

Please replace paragraph [0037] with the following:

[0037] The output DMA controller 178 in box 210 may create a frame 146 based upon the pace signal and may store the created frame 146 in the output buffer 150 for delivery to the codecs 112. In one embodiment, the output DMA controller 178 may read data for the frame 146 from the memory 104 per the buffer descriptor list provided by the processor 100. Further, in response to the pace signal indicating that further commands to the codecs 112 is permitted, the output DMA controller 178 may read a command from the command buffer 116 of the memory 104. In one embodiment, the output DMA controller 178 may read the command from the command entry 124 identified by the read pointer 132 and may update the read pointer 132 to identify a subsequent command entry 124 of the command buffer 116. The output DMA controller 178 may further create a frame 146 based upon the read data and command (if any) and ~~store~~ store the frame 146 in the output buffer 150 for delivery to one or more codecs 112.

Please replace paragraph [0040] with the following:

[0040] In box ~~214~~ 246, the output DMA controller 178 may determine whether it has reached the end of the stream. In response to the output DMA controller 178 determining that the end of the stream has not yet been reached, the audio controller 106 may return to box 208 in order for the command pacer 182 to update the pace signal and the output DMA controller 178 to create another frame 146 for the codecs 112 based upon the updated pace signal.